

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	722	365/185.2.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/01/21 14:43
L2	1029	365/185.21.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/01/21 14:44
L3	1292	365/210.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/01/21 14:44
L4	77	pasotti-marco.in.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/01/21 14:44
L5	3	sandre-guido-de.in.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/01/21 14:44
L6	19	iezzi-david.in.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/01/21 14:44

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L4	77	pasotti-marco.in.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/01/21 14:44
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L6	19	iezzi-david.in.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/01/21 14:44

Day : Sunday
Date: 1/21/2007


PALM INTRANET

Time: 14:45:02

Inventor Name Search Result

Your Search was:

Last Name = PASOTTI

First Name = MARCO

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>08722572</u>	<u>5805492</u>	150	09/27/1996	ANALOG MEMORY FOR STORING A QCIF IMAGE OR THE LIKE AS ELECTRIC CHARGE	PASOTTI, MARCO
<u>09060165</u>	<u>6016272</u>	150	04/14/1998	HIGH-PRECISION ANALOG READING CIRCUIT FOR FLASH ANALOG MEMORY ARRAYS USING NEGATIVE FEEDBACK	PASOTTI, MARCO
<u>09076013</u>	<u>6195283</u>	150	05/11/1998	METHOD AND DEVICE FOR ANALOG PROGRAMMING OF NON-VOLATILE MEMORY CELLS	PASOTTI, MARCO
<u>09121024</u>	<u>5973959</u>	150	07/22/1998	CIRCUIT AND METHOD OF READING CELLS OF AN ANALOG MEMORY ARRAY, IN PARTICULAR OF THE FLASH TYPE	PASOTTI, MARCO
<u>09162639</u>	<u>6081448</u>	150	09/28/1998	METHOD AND DEVICE FOR ANALOG PROGRAMMING OF FLASH EEPROM MEMORY CELLS WITH AUTOVERIFY	PASOTTI, MARCO
<u>09181230</u>	<u>6069822</u>	150	10/27/1998	METHOD FOR PARALLEL PROGRAMMING OF NONVOLATILE MEMORY DEVICES, IN PARTICULAR FLASH MEMORIES AND EEPROMS	PASOTTI, MARCO
<u>09185906</u>	<u>6011715</u>	150	11/03/1998	METHOD FOR MULTILEVEL PROGRAMMING OF A NONVOLATILE MEMORY, AND A MULTILEVEL NONVOLATILE MEMORY	PASOTTI, MARCO
<u>09234942</u>	<u>6091642</u>	150	01/21/1999	METHOD FOR CONTROLLED	PASOTTI, MARCO

				ERASING MEMORY DEVICES, IN PARTICULAR ANALOG AND MULTI-LEVEL FLASH- EEPROM DEVICES	
<u>09239027</u>	<u>6034888</u>	150	01/27/1999	READING CIRCUIT FOR NONVOLATILE ANALOG MEMORIES, IN PARTICULAR FLASH-EEPROM MEMORIES, WITH DIRECT AND CONSTANT CURRENT THRESHOLD VOLTAGE READING	PASOTTI, MARCO
<u>09342932</u>	Not Issued	161	06/29/1999	VLSI ARCHITECTURE FOR IMPLEMENTING VARIABLE LENGTH CODING IN A DIGITAL VIDEO IMAGE SYSTEM	PASOTTI, MARCO
<u>09397387</u>	<u>6169691</u>	150	09/15/1999	METHOD FOR MAINTAINING THE MEMORY CONTENT OF NON-VOLATILE MEMORY CELLS	PASOTTI, MARCO
<u>09421616</u>	<u>6323799</u>	150	10/19/1999	DEVICE FOR READING NON- VOLATILE MEMORY CELLS, IN PARTICULAR ANALOG FLASH MEMORY CELLS	PASOTTI, MARCO
<u>09425446</u>	<u>6195289</u>	150	10/22/1999	DEVICE FOR READING ANALOG NONVOLATILE MEMORY CELLS, IN PARTICULAR FLASH CELLS	PASOTTI, MARCO
<u>09438232</u>	<u>6466481</u>	150	11/12/1999	DEVICE AND METHOD FOR PROGRAMMING NONVOLATILE MEMORY CELLS WITH AUTOMATIC GENERATION OF PROGRAMMING VOLTAGE	PASOTTI, MARCO
<u>09438823</u>	<u>6128228</u>	150	11/12/1999	CIRCUIT FOR HIGH- PRECISION ANALOG READING OF NONVOLATILE MEMORY CELLS, IN PARTICULAR ANALOG OR MULTILEVEL FLASH OR EEPROM MEMORY CELLS	PASOTTI, MARCO
<u>09447531</u>	<u>6163483</u>	150	11/23/1999	CIRCUIT FOR PARALLEL PROGRAMMING NONVOLATILE MEMORY CELLS, WITH ADJUSTABLE PROGRAMMING SPEED	PASOTTI, MARCO

<u>09449168</u>	<u>6392931</u>	150	11/24/1999	METHOD FOR HIGH PRECISION PROGRAMMING NONVOLATILE MEMORY CELLS, WITH OPTIMIZED PROGRAMMING SPEED	PASOTTI, MARCO
<u>09467726</u>	<u>6232753</u>	150	12/20/1999	VOLTAGE REGULATOR FOR DRIVING PLURAL LOADS BASED ON THE NUMBER OF LOADS BEING DRIVEN	PASOTTI, MARCO
<u>09553526</u>	<u>6282125</u>	150	04/20/2000	Method for erasing and rewriting non volatile memory cells and particularly flash cells	PASOTTI, MARCO
<u>09569232</u>	<u>6292398</u>	150	05/11/2000	Method for the in-writing verification of the threshold value in non-volatile memories	PASOTTI, MARCO
<u>09592796</u>	<u>6314043</u>	150	06/13/2000	Erasing and parallel rewriting circuit for memory cell blocks, particularly for analog flash cells, and related operating method	PASOTTI, MARCO
<u>09616507</u>	<u>6259626</u>	150	07/14/2000	Method for storing bytes in multi-level non-volatile memory cells	PASOTTI, MARCO
<u>09699043</u>	<u>6473340</u>	150	10/27/2000	READING METHOD AND CIRCUIT FOR A NON-VOLATILE MEMORY	PASOTTI, MARCO
<u>09699304</u>	<u>6400607</u>	150	10/27/2000	READING CIRCUIT FOR A NON-VOLATILE MEMORY	PASOTTI, MARCO
<u>09699309</u>	<u>6381177</u>	150	10/27/2000	METHOD FOR CONTROLLED SOFT PROGRAMMING OF NON-VOLATILE MEMORY CELLS, IN PARTICULAR OF THE FLASH EEPROM AND EPROM TYPE	PASOTTI, MARCO
<u>09738253</u>	<u>6469934</u>	150	12/14/2000	SOFT PROGRAMMING METHOD FOR NON-VOLATILE MEMORY CELLS	PASOTTI, MARCO
<u>09788282</u>	Not Issued	61	02/16/2001	Electronic device for the recording/reproduction of voice data	PASOTTI, MARCO
<u>09817804</u>	<u>6581134</u>	150	03/26/2001	LOGIC PARTITIONING OF A NONVOLATILE MEMORY ARRAY	PASOTTI, MARCO
<u>09833754</u>	<u>6622106</u>	150	04/11/2001	DEVICE FOR TESTING AND CALIBRATING THE OSCILLATION FREQUENCY OF AN INTEGRATED OSCILLATOR	PASOTTI, MARCO

<u>09844252</u>	Not Issued	160	04/27/2001	Method for storing bytes in multi-level non-volatile memory cells	PASOTTI, MARCO
<u>09882535</u>	<u>6535428</u>	150	06/14/2001	BIASING CIRCUIT FOR MULTI-LEVEL MEMORY CELLS	PASOTTI, MARCO
<u>09920316</u>	Not Issued	161	08/01/2001	Electronic circuit for controlling voltage signals at particular critical nodes, for example of a POR circuit	PASOTTI, MARCO
<u>09953591</u>	Not Issued	161	09/14/2001	Method for storing bytes in multi-level non-volatile memory cells	PASOTTI, MARCO
<u>10017502</u>	<u>6667903</u>	150	12/14/2001	PROGRAMMING METHOD FOR A MULTILEVEL MEMORY CELL	PASOTTI, MARCO
<u>10035909</u>	<u>6655758</u>	150	12/19/2001	METHOD FOR STORING DATA IN A NONVOLATILE MEMORY	PASOTTI, MARCO
<u>10036088</u>	<u>6839818</u>	150	12/28/2001	ELECTRICALLY MODIFIABLE, NON-VOLATILE, SEMICONDUCTOR MEMORY WHICH CAN KEEP A DATUM STORED UNTIL AN OPERATION TO MODIFY THE DATUM IS COMPLETED	PASOTTI, MARCO
<u>10036337</u>	<u>6687159</u>	150	12/19/2001	METHOD OF PROGRAMMING A PLURALITY OF MEMORY CELLS CONNECTED IN PARALLEL, AND A PROGRAMMING CIRCUIT THEREFOR	PASOTTI, MARCO
<u>10082367</u>	Not Issued	160	02/25/2002	Method for storing bytes in multi-level non-volatile memory cells	PASOTTI, MARCO
<u>10225513</u>	<u>6687167</u>	150	08/20/2002	EEPROM FLASH MEMORY ERASABLE LINE BY LINE	PASOTTI, MARCO
<u>10331161</u>	<u>7088614</u>	150	12/26/2002	PROGRAMMING METHOD FOR A MULTILEVEL MEMORY CELL	PASOTTI, MARCO
<u>10672923</u>	Not Issued	161	09/26/2003	Current source for low voltage applications	PASOTTI, MARCO
<u>10728372</u>	<u>6894934</u>	150	12/04/2003	NON-VOLATILE MEMORY CELL SENSING CIRCUIT, PARTICULARLY FOR LOW POWER SUPPLY VOLTAGES AND HIGH CAPACITIVE LOAD VALUES	PASOTTI, MARCO
<u>10748697</u>	<u>6897710</u>	150	12/30/2003	VOLTAGE SUPPLY	PASOTTI, MARCO

				DISTRIBUTION ARCHITECTURE FOR A PLURALITY OF MEMORY MODULES	
<u>10768401</u>	Not Issued	41	01/30/2004	Reconfigurable signal processing IC with an embedded flash memory device	PASOTTI, MARCO
<u>10768743</u>	Not Issued	41	01/29/2004	Embeddable flash memory system for non-volatile storage of code, data and bit-streams for embedded FPGA configurations	PASOTTI, MARCO
<u>10777457</u>	Not Issued	71	02/12/2004	Sense amplifier for low-supply- voltage nonvolatile memory cells	PASOTTI, MARCO
<u>10816271</u>	<u>7110289</u>	150	03/31/2004	METHOD AND SYSTEM FOR CONTROLLING MRAM WRITE CURRENT TO REDUCE POWER CONSUMPTION	PASOTTI, MARCO
<u>11321732</u>	Not Issued	30	12/28/2005	Level shifter translator	PASOTTI, MARCO

Inventor Search Completed: No Records to Display.

	Last Name	First Name	
Search Another: Inventor	<input type="text" value="pasotti"/>	<input type="text" value="marco"/>	<input type="button" value="Search"/>

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Day : Sunday
Date: 1/21/2007

Time: 14:45:25

 PALM INTRANET**Inventor Name Search Result**

Your Search was:

Last Name = SANDRE

First Name = GUIDO

Application#	Patent#	Status	Date Filed	Title	Inventor Name
09699304	6400607	150	10/27/2000	READING CIRCUIT FOR A NON-VOLATILE MEMORY	SANDRE, GUIDO DE
09699309	6381177	150	10/27/2000	METHOD FOR CONTROLLED SOFT PROGRAMMING OF NON-VOLATILE MEMORY CELLS, IN PARTICULAR OF THE FLASH EEPROM AND EPROM TYPE	SANDRE, GUIDO DE
09882535	6535428	150	06/14/2001	BIASING CIRCUIT FOR MULTI-LEVEL MEMORY CELLS	SANDRE, GUIDO DE
10225513	6687167	150	08/20/2002	EEPROM FLASH MEMORY ERASABLE LINE BY LINE	SANDRE, GUIDO DE
10777457	Not Issued	71	02/12/2004	Sense amplifier for low-supply-voltage nonvolatile memory cells	SANDRE, GUIDO DE

Inventor Search Completed: No Records to Display.

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Day : Sunday
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 PALM INTRANET**Inventor Name Search Result**

Your Search was:

Last Name = IEZZI

First Name = DAVID

Application#	Patent#	Status	Date Filed	Title	Inventor Name
09882535	6535428	150	06/14/2001	BIASING CIRCUIT FOR MULTI-LEVEL MEMORY CELLS	IEZZI, DAVID
10017502	6667903	150	12/14/2001	PROGRAMMING METHOD FOR A MULTILEVEL MEMORY CELL	IEZZI, DAVID
10035909	6655758	150	12/19/2001	METHOD FOR STORING DATA IN A NONVOLATILE MEMORY	IEZZI, DAVID
10036337	6687159	150	12/19/2001	METHOD OF PROGRAMMING A PLURALITY OF MEMORY CELLS CONNECTED IN PARALLEL, AND A PROGRAMMING CIRCUIT THEREFOR	IEZZI, DAVID
10225513	6687167	150	08/20/2002	EEPROM FLASH MEMORY ERASABLE LINE BY LINE	IEZZI, DAVID
10331161	7088614	150	12/26/2002	PROGRAMMING METHOD FOR A MULTILEVEL MEMORY CELL	IEZZI, DAVID
10748697	6897710	150	12/30/2003	VOLTAGE SUPPLY DISTRIBUTION ARCHITECTURE FOR A PLURALITY OF MEMORY MODULES	IEZZI, DAVID
10777457	Not Issued	71	02/12/2004	Sense amplifier for low-supply-voltage nonvolatile memory cells	IEZZI, DAVID

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